

EAST - [896071b:1]

FileViewEditToolsWindowHelp

Active

L1: (1067) evaluat\$4 same cell same design

L2: (6002) cell near9 specification

L3: (52) l1 and l2

L4: (6) l3 and hdl and netlist

L5: (4) l4 and context

L6: (3) l5 and library

L7: (2) l5 and library and map\$4

L8: (2) l5 and map\$4

L9: (1531) ("transistor-level" or (transistor adj3 level))same cell

L10: (2) l4 and l9

L11: (21832) cell near9 design

L12: (9293) map\$4 near9 cell

L13: (16) l12 and l11 and l9

L14: (494) library and hdl and netlist

L15: (3) l13 and l14

Failed

Saved

SearchListBrowseQueue

DBsUSPlural

Default operatorHighlight all hit terms initially

l13 and l14

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020073380 A1	20020613	89	Block based design methodology with programmable components	716/1
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20020069396 A1	20020606	20	Method for automated design of integrated circuits with targeted quality objectives	716/7
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6083271 A	20000704	15	Method and apparatus for specifying multiple power domains in electronic circuit designs	716/1

HitsDetailsHTML

Ready

NUM

The screenshot displays a patent search application window. The top menu bar includes 'File', 'View', 'Edit', 'Tools', 'Window', and 'Help'. Below the menu is a toolbar with various icons. The main window is divided into two panes. The left pane shows a tree view of search results, with 'Active' selected. The right pane shows a search results list with columns for 'Document ID', 'Issue Date', 'Pages', 'Title', and 'Current OR'. The search results list contains two entries. Below the search results list is a table with columns for 'U', '1', 'Document ID', 'Issue Date', 'Pages', 'Title', and 'Current OR'. The table contains two rows of data. The bottom status bar shows 'Ready' and 'NLM'.

Search Results List:

Document ID	Issue Date	Pages	Title	Current OR
US 20020069396 A1	20020606	20	Method for automated design of integrated circuits with targeted quality objectives	716/7
US 20020053063 A1	20020502	13	Process for automated generation of design-specific complex functional blocks to	716/1

Table:

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<input type="checkbox"/>	<input type="checkbox"/>	US 20020069396 A1	20020606	20	Method for automated design of integrated circuits with targeted quality objectives	716/7
<input type="checkbox"/>	<input type="checkbox"/>	US 20020053063 A1	20020502	13	Process for automated generation of design-specific complex functional blocks to	716/1